<u>IN THE SPECIFICATION:</u>

Please amend the specification as follows:

Paragraph starting at page 8, line 11:

Figure 4 illustrates a receiver utilizing a FEC coder for pseudo error monitoring, and Figure 5 illustrates a transmitter sending coded bit stream to the receiver of Figure 4. In the receiver, the received signal is supplied through the front end of the receiver (not shown in the figure) to a quadrature mixer 41 in which the signal is converted in a known manner by means of the signal of a local oscillator LO to baseband. If an intermediate frequency is used in the reception, the signal SR supplied to the mixer is an IF signal, or an RF signal is supplied if direct conversion to the baseband is used. Quadrature baseband signals I and Q output from the mixer are amplified and filtered in an amplifier stage 42 before being supplied to a decision-making unit 43. The decision-making unit determines which symbols are being received. The symbol stream is then decoded in a symbol decoder 44, whereby the original FEC coded bit stream DATA_F is received from the decoder output. This bit stream is then supplied to a FEC decoder 45, which performs forward error decoding by removing, in a known manner, the redundancy which is added to the baseband bit stream in the FEC coder 51 50 in the transmitter (Figure 5). The FEC decoder has two outputs: the first output is for the corrected bit stream DATA and the second output is for the error signal (PE) indicating the corrections that the decoder has made. According to the invention, the corrections made the decoder are

interpreted as pseudo errors by the control unit, which then adjusts the transmission power of the transmitter according to the above-described algorithm.